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There are several ways of optimizing for high reliability and thermal performance," said Ouyang. Typically, packages are designed with certain maximum levels. Ouyang notes that system integrators might specify that thermal resistances  $\theta_{ja}$ ,  $\theta_{jc}$  and  $\theta_{jb}$ , not exceed certain values. Commonly, silicon junction temperature is kept below 125°C.

### Keeping IC Packages Cool

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Answer (1 of 5): These are called the 'hybrid parameters'. These parameters are based on the hybrid model for studying the operation of a transistor amplifier in the 'low voltage' region, also called 'small signal operation'.  $h_{fe}$  : It is called the current gain  $h_{ie}$  : It is the internal resistance...

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